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By: Lauren D. PearsonDate: 6/21/02IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Wilfried Daehn

Appl. No. : 09/484,781

Filed : January 18, 2000

Title : Integrated Semiconductor Circuit and Method
for Functional Testing of Pad Cells

Examiner : Don Le

Group Art Unit : 2819

A M E N D M E N T

Hon. Commissioner of Patents and Trademarks
Washington, DC 20231

S i r :

Responsive to the Office action dated March 21, 2002, kindly
amend the above-identified application as follows:

In the Claims:

Cancel claim 3.

Claim 1 (twice amended). An integrated semiconductor circuit,
comprising:

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a plurality of pad cells to be monitored in one operating mode by a functional test, said plurality of pad cells each having a connecting pad, an upstream output driver, and a connection for an input signal;

a signal transmitter for producing periodic signal sequences, said signal transmitter having a connection for a periodic

output signal connected to said connections for an input signal of said plurality of pad cells to be tested, in order to test a transmission response of said plurality of pad cells in said one operating mode; and

shift register cells each connected in series between said connection for an input signal of a respective one of said plurality of pad cells and said connection of said signal transmitter for an output signal;

said connections for an input signal of said plurality of pad cells being connected in parallel to said connection of said signal transmitter for an output signal.

Claim 7 (amended). An integrated semiconductor circuit, comprising:

a plurality of pad cells to be monitored in one operating mode by a functional test, said plurality of pad cells each having

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a connecting pad, an upstream output driver, and a connection for an input signal;

) a signal transmitter for producing periodic signal sequences, said signal transmitter having a clock-controlled bistable multivibrator of the T-flip-flop type and a connection for a periodic output signal connected to said connection for an

input signal of said plurality of pad cells to be tested, in order to test a transmission response of said plurality of pad cells in said one operating mode; and

shift register cells each connected in series between said connection for an input signal of a respective one of said plurality of pad cells and said connection of said signal transmitter for an output signal.

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Remarks:

Reconsideration of the application is requested.

Claims 1-2 and 5-10 are now in the application. Claims 1 and 7 have been amended. Claim 3 has been canceled.

In item 8 of the Office action, the Examiner rejected claims
1-2, 5-6, and 8-10 as being fully anticipated by Kamada (U.S. 5,621,740) under 35 U.S.C. § 102(b). However, in item 8 of the Office action, the Examiner objected to claims 3 and 7 but indicated that they contained allowable subject matter.

Accordingly, the features of claim 3 have been added to claim 1. Therefore, amended claim 1 and the claims depending therefrom are allowable.

Claim 7 has been amended to be an independent claim containing all of the features of its base claim. Therefore, claim 7 should be allowed. In addition, the claim depending on claim 7 should also be allowed.

In view of the foregoing, reconsideration and allowance of claims 1-2 and 5-10 are solicited.

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In the event the Examiner should still find any of the claims to be unpatentable, please telephone counsel so that patentable language can be substituted.

Please charge any other fees that might be due with respect to Sections 1.16 and 1.17 to the Deposit Account of Lerner and Greenberg, P.A., No. 12-1099.

Respectfully submitted,

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For Applicant

LDP:cgm

June 21, 2002

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The stamp of the Patent Office hereon may be considered the date on which
papers indicated below were received.

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